

BHAVAN'S VIVEKANANDA COLLEGE, SAINIKPURI
FACULTY OF PHYSICAL SCIENCES
PRACTICAL FINAL EXAMINATION TIME TABLE - 2022 - 23

DATE	SUB	MATHEMATICS	PHYSICS	ELECTRONICS	STATISTICS	COMPUTER SCIENCE
19-04-2023	FN	****	****	MECS 2 B - BATCH 4	MSCS 2 A - BATCH1, HDS 2 - BATCH 1	MSCS 1 A, HDS 1 - BATCH 1
	AN	****	****	MECS 2 B - BATCH 5	MSCS 2 A - BATCH2, HDS 2 - BATCH 2	MSCS 1 B, HDS 1 - BATCH 2
20-04-2023	FN	****	****	MECS 2 B - BATCH 6	MSCS 2 B - BATCH1, HDS 1 - BATCH 1	MPCS 1
	AN	****	****	MECS 2 A - BATCH 1	MSCS 2 B - BATCH2, HDS 1 - BATCH 2	MECS 1
21-04-2023	FN	****	****	MECS 2 A - BATCH 2	MSCS 3 A	MPCS 2 A, HDS 2 - BATCH 1
	AN	****	****	MECS 2 A - BATCH 3	MSCS 3 B	MSCS 2 A, HDS 2 - BATCH 2
22-04-2023	FN	RAMZAN				
	AN	RAMZAN				
23-04-2023	FN	SUNDAY				
	AN	SUNDAY				
24-04-2023	FN	****	MPCS 1 - BATCH 1, MPCS 2 A - BATCH 1	MECS 1 - BATCH 1, MECS 3 A - BATCH 1	MSCS 1 A	MPCS 2 B, MECS 3 B - BATCH 4 & 5
	AN	****	MPCS 1 - BATCH 2, MPCS 2 A - BATCH 2	MECS 1 - BATCH 2, MECS 3 A - BATCH 2	MSCS 1 B	MECS 2 B, MECS 3 B - BATCH 6
25-04-2023	FN	****	MPCS 3 - BATCH 1, MPCS 2 A - BATCH 3	MECS 3 A - BATCH 3	****	MECS 2 A, HDS 1 - BATCH 1
	AN	****	MPCS 3 - BATCH 2, MPCS 2 B - BATCH 4	MECS 3 B - BATCH 4	****	MSCS 2 B, HDS 1 - BATCH 2
26-04-2023	FN	HDS 2 (MATLAB)	MPCS 2 B - BATCH 5 MPCS 1 - BATCH 3	MECS 3 B - BATCH 5	****	MPCS 3
	AN	****	MPCS 2 B - BATCH 6 MPCS 1 - BATCH 4	MECS 3 B - BATCH 6	****	MSCS 3 A

for 3/4/23.

3/4/23.

3/4

N. S. Shukla 3/4/23

Dr. JESUREK CHOUDARY
PRINCIPAL
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Sainikpuri-500094,
Medchal District, Telangana State

27-04-2023	FN	****	MPCS 3 - BATCH 3	****	****	MECS 3 A
	AN	****	****	****	****	MSCS 3 B
	FN					
	AN					

FN 10:00 AM TO 01:00 PM
AN 01:30 PM TO 04:30 PM

Ramare
12/4/23
CONTROLLER OF EXAMINATIONS
Bhavan's Vivekananda College
of Science, Humanities & Commerce
(Autonomous, Accredited with 'A' Grade by NAAC)
Sainikpuri, Secunderabad

R
3/4/23
FACULTY IN CHARGE

1/10/23
3/4/23
PRINCIPAL
Dr. GSVRK CHOUDARY
PRINCIPAL
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OF SCIENCE, HUMANITIES & COMMERCE
Sainikpuri, Secunderabad
Medchal District, Telangana State

- Note**
- 1 Students having Backlog examinations in practicals are requested to report to the concerned HOD on or before 15th April 2023
 - 2 Digital System Design with VHDL - Practical examination is scheduled on 17 - 04 - 2023 FN

Jr.
3/4/2023

- 3 FN Reporting time : 9:30 AM
AN Reporting time : 12:30 PM